Divide and recycle: types and compilation for a hybrid synchronous language [a]

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[a] Joint work with Albert Benveniste, Timothy Bourke and Benoit Caillaud

Motivation and Context

- **Hybrid modelers** allow to program both a (discrete) controler and its physical (continuous) environment in the very same language.
- **Explicit** *vs* **implicit** modelers: Simulink, Scicos *vs* Modelica, SimScape.
- In this talk, we consider only explicit ones.
- Many results on the formal verification of the sub-class of **hybrid automata** ([CBPSV04]) but less on programming language aspects.

Objective:

- **Extend** a synchronous language where dataflow equations are mixed with ODE.
- Make it **conservative**, i.e., nothing must change for the discrete subset (same typing, same code generation).

Contribution:

- **Divide** with a type system.
- **Recycle** existing tools, synchronous compilers and numerical solvers to execute them.

Parallel composition: homogeneous case

Two equations with discrete time:

```
f = 0.0 -> pre f + s and s = 0.2 * (x - pre f)
```

and the initial value problem:

```
der(y') = -9.81 init 0.0 and der(y) = y' init 10.0
```

The first program can be written in any synchronous language, e.g. LUSTRE.

$$\forall n \in \mathbb{N}^*, f_n = f_{n-1} + s_n \text{ and } f_0 = 0 \qquad \forall n \in \mathbb{N}, s_n = 0.2 * (x_n - f_{n-1})$$

The second program can be written in any hybrid modeler, e.g. SIMULINK.

$$\forall t \in \mathbb{R}_+, y'(t) = 0.0 + \int_0^t -9.81\, dt = -9.81\, t$$

$$\forall t \in \mathbb{R}_+, y(t) = 10.0 + \int_0^t y'(t)\, dt = 10.0 - 9.81 \int_0^t t\, dt$$

Parallel composition is clear since equations **share the same time scale**.

Parallel composition: heterogeneous case

Two equations: a signal defined at discrete instants, the other continuously.

```
der time = 1.0 init 0.0 and x = 0.0 fby x + time
```

or:

```
x = 0.0 fby x +. 1.0 and der y = x init 0.0
```

It would be tempting to define the first equation as: $\forall n \in \mathbb{N}, x_n = x_{n-1} + \mathtt{time}(n)$

And the second as:

$$\forall n \in \mathbb{N}^*, x_n = x_{n-1} + 1.0 \text{ and } x_0 = 1.0$$

$$\forall t \in \mathbb{R}_+, y(t) = 0.0 + \int_0^t x(t)\,dt$$

i.e., $x(t)$ as a piecewise constant function from $\mathbb{R}_+$ to $\mathbb{R}_+$ with $\forall t \in \mathbb{R}_+, x(t) = x_{\lfloor t \rfloor}$.

In both cases, this would be a mistake. `x` is defined on a discrete, logical time; `time` on a continuous, absolute time.

Equations with reset

Two independent groups of equations.

```
  der p = 1.0 init 0.0 reset 0.0 every up(p - 1.0)
and
  x = 0.0 fby x + p
and
  der time = 1.0 init 0.0
and
  z = up(sin (freq * time))
```

Properly translated in Simulink, changing `freq` changes the output of `x`!

If `f` is running on a continuous time basis, what would be the meaning of:

```
  y = f(x) every up(z) init 0
```

All these programs are **wrongly typed** and should be statically rejected.

Discrete *vs* Continuous time signals

A signal is discrete if it is activated on a discrete clock.

A clock is termed *discrete* if it has been declared so or if it is the result of a zero-crossing or a sub-sampling of a discrete clock. Otherwise, it is termed *continuous*.

Notation

- `up`(e) tests the zero-crossing of expression e (from negative to positive).
- If $x =$ `up`(e), all handlers using x are governed by the same zero-crossing.
- Handlers have priorities.

```
z = 1 every up(x) | 2 every up(y) init 0
```

- `last`(x) for the left-limit of signal x.

```
z = last z + 1 every up(x) | last z - 1 every up(y) init 0
```

Examples

Combinatorial and sequential function (discrete time).

```
let add (x,y) = x + y


let node counter(top, tick) = o where
     o = if top then i else 0 fby o + 1
  and i = if tick then 1 else 0


let edge x = false -> pre x <> x
```

- `add` get type signature: $\texttt{int} \times \texttt{int} \xrightarrow{\texttt{A}} \texttt{int}$
- `counter` get type signature: $\texttt{bool} \times \texttt{bool} \xrightarrow{\texttt{D}} \texttt{int}$
- `edge` get type signature: $\forall \alpha . \alpha \xrightarrow{\texttt{D}} \texttt{bool}$

Connecting a discrete to continuous time

```
let hybrid counter_ten(top, tick) = o where
  (* a periodic timer 2.1(4) *)
      der(time) = 1.0 init -2.1 reset -4 every z
  and z = up(time)
  (* discrete function *)
  and o = counter(top, tick) every z init 0
```

The type signature is: $\texttt{bool} \times \texttt{bool} \xrightarrow{\texttt{C}} \texttt{int}$.

Remark: provide ad-hoc programming constructs for periodic timers.

The Bouncing ball

```
let hybrid bouncing(x0,y0,x'0,y'0) = (x,y) where
    der(x) = x' init x0
 and
    der(x') = 0.0 init x'0
 and
    der(y) = y' init y0
 and
    der(y') = -. g init y'0 reset -. 0.8 *. last y' every up(-. y)
```

Its type signature is: $\texttt{float} \times \texttt{float} \times \texttt{float} \xrightarrow{\texttt{C}} \texttt{float} \times \texttt{float}$

The language kernel

- Synchronous (discrete) data-flow operators.
- Ordinary Differential Equations (ODE) with reset handlers

$$
\begin{array}{lcl}
d & ::= & \texttt{let}\ k\ f(p) = e \mid d; d \\
e & ::= & x \mid v \mid op(e) \mid e\ \texttt{fby}\ e \mid \texttt{last}(x) \\
 & & \quad \mid \texttt{up}(e) \mid f(e) \mid (e, e) \mid \texttt{let}\ E\ \texttt{in}\ e \\
p & ::= & (p, p) \mid x \\
h & ::= & e\ \texttt{every}\ e \parallel \ldots \parallel e\ \texttt{every}\ e \\
E & ::= & x = e \mid \texttt{der}(x) = e\ \texttt{init}\ e\ \texttt{reset}\ h \\
 & & \qquad \mid x = h\ \texttt{default}\ e\ \texttt{init}\ e \\
 & & \qquad \mid x = h\ \texttt{init}\ e \mid E\ \texttt{and}\ E
\end{array}
$$

Typing

The type language

$$\begin{array}{lcl} \sigma & ::= & \forall \beta_1, ..., \beta_n . t \xrightarrow{k} t \\ t & ::= & t \times t \mid \beta \mid bt \\ k & ::= & \mathtt{D} \mid \mathtt{C} \mid \mathtt{A} \\ bt & ::= & \mathtt{float} \mid \mathtt{int} \mid \mathtt{bool} \mid \mathtt{zero} \end{array}$$

We consider only a first order language. Extension to higher-order later.

Initial conditions

$$\begin{array}{lcl} (+) & : & \mathtt{int} \times \mathtt{int} \xrightarrow{\mathtt{A}} \mathtt{int} \\ (=) & : & \forall \beta . \beta \times \beta \xrightarrow{\mathtt{A}} \mathtt{bool} \\ \mathtt{if} & : & \forall \beta . \mathtt{bool} \times \beta \times \beta \xrightarrow{\mathtt{A}} \beta \\ \mathtt{pre}(.) & : & \forall \beta . \beta \xrightarrow{\mathtt{D}} \beta \\ .\,\mathtt{fby}\,. & : & \forall \beta . \beta \times \beta \xrightarrow{\mathtt{D}} \beta \\ \mathtt{up}(.) & : & \mathtt{float} \xrightarrow{\mathtt{C}} \mathtt{zero} \end{array}$$

The Type system

Global and local environment

$$G ::= [f_1 : \sigma_1; ...; f_n : \sigma_n] \qquad H ::= [\,] \mid H, x : t \mid H, \texttt{last}(x) : t$$

Typing predicates

- $G, H \vdash_k e : t$: Expression e has type t and kind k. $G, H \vdash_k e : t$
- $H, H \vdash_k E : H'$: Equation E produces environment H' and has kind k.

Subtyping

An combinatorial function can be passed where a discrete or continuous one is expected:

$$\forall k, \texttt{A} \leq k$$

A sketch of Typing rules

(DER)

$$\frac{G, H \vdash_{\mathtt{C}} e_1 : \mathtt{float} \qquad G, H \vdash_{\mathtt{C}} e_2 : \mathtt{float} \qquad G, H \vdash h : \mathtt{float}}{G, H \vdash_{\mathtt{C}} \mathtt{der}(x) \ = \ e_1 \ \mathtt{init} \ e_2 \ \mathtt{reset} \ h : [\mathtt{last}(x) : \mathtt{float}]}$$

(AND)

$$\frac{G, H \vdash_k E_1 : H_1 \qquad G, H \vdash_k E_2 : H_2}{G, H \vdash_k E_1 \ \mathtt{and} \ E_2 : H_1 + H_2}$$

(EQ)

$$\frac{G, H \vdash_k e : t}{G, H \vdash_k x \ = \ e : [x : t]}$$

(APP)

$$\frac{t \xrightarrow{k} t' \in \mathit{Inst}(G(f)) \qquad G, H \vdash_k e : t}{G, H \vdash_k f(e) : t'}$$

A sketch of the semantics

The sets ${}^\star\mathbb{R}$ and ${}^\star\mathbb{N}$ as the non-standard extensions of $\mathbb{R}$ and $\mathbb{N}$.

- ${}^\star\mathbb{N}$ contains elements that are infinitely large (${}^\star n > n$ for any $n \in \mathbb{N}$).
- ${}^\star\mathbb{R}$ contains elements that are *infinitesimal*, $0 < \partial < t$ for any $t \in \mathbb{R}_+$.

The base clock: ∂ infinitesimal, the set

$$BaseClock(\partial) = \{n\partial \mid n \in {}^\star\mathbb{N}\}$$

is isomorphic to ${}^\star\mathbb{N}$ as a total order. For every $t \in \mathbb{R}_+$ and any $\epsilon > 0$, there exists $t' \in BaseClock(\partial)$ such that $|t' - t| < \epsilon$ expressing that $BaseClock(\partial)$ is dense in $\mathbb{R}_+$.

$BaseClock(\partial)$ is a natural candidate for a time index set and ∂ is the corresponding time basis.

For $t = t_n = n\partial \in BaseClock(\partial)$, ${}^\bullet t = t_{n-1}$ and $t^\bullet = t_{n+1}$.

A sketch of the semantics

Reason "as if" the time was discrete and global. The idea of using non standard analysis for the semantics of systems has been recognized by Bliudze et Krob.

Clock and signals A *clock* T is a subset of $BaseClock(\partial)$. A *signal* s is a total function $s : T \mapsto V$.

If T is a clock and b a signal $b : T \mapsto \mathbb{B}$, then T `on` b defines a subset of T comprising those instants where $b(t)$ is true:

$$T \texttt{ on } b = \{t \mid (t \in T) \wedge (b(t) = \texttt{true})\}$$

If $s : T \mapsto {}^{\star}\mathbb{R}$, we write T `on up`(s) for the instants when s crosses zero, that is:

$$T \texttt{ on up}(s) = \{t^{\bullet} \mid (t \in T) \wedge (s({}^{\bullet}t) \leq 0) \wedge (s(t) > 0)\}$$

The effect of `up`(e) is delayed by one cycle.

Discrete *vs* Continuous

Let x be a signal with clock domain T_x, it is typed *discrete* ($\mathtt{D}(T)$) either if it has been so declared, or if its clock is the result of a zero-crossing or a sub-clock of a discrete clock. Otherwise it is typed *continuous* ($\mathtt{C}(T)$). That is:

1. $\mathtt{C}(BaseClock(\partial))$
2. If $\mathtt{C}(T)$ and $s : T \mapsto {}^{\star}\mathbb{R}$ then $\mathtt{D}(T \;\mathtt{on}\; \mathtt{up}(s))$
3. If $\mathtt{D}(T)$ and $s : T \mapsto \mathbb{B}$ then $\mathtt{D}(T \;\mathtt{on}\; s)$
4. If $\mathtt{C}(T)$ and $s : T \mapsto \mathbb{B}$ then $\mathtt{C}(T \;\mathtt{on}\; s)$

Correctness of the type system:

When an is typed $\mathtt{D}$ (resp. $\mathtt{C}$), it is indeed activated on a discrete (resp. continuous) clock.

Continuous Operators

T must be a continuous clock, that is: $\mathtt{C}(T)$.

$$
\begin{array}{lcll}
\mathit{integr}^{\#}(T)(s)(s_0)(hs)(t) & = & s'(t) & \text{where} \\
s'(t) & = & s_0(t) & \text{if } t = \min(T) \\
s'(t) & = & s'({}^{\bullet}t) + \partial s({}^{\bullet}t) & \text{if } \mathit{handler}^{\#}(T)(hs)(t) = \mathit{NoEvent} \\
s'(t) & = & v & \text{if } \mathit{handler}^{\#}(T)(hs)(t) = \mathit{Xcrossing}(v)
\end{array}
$$

$$
\begin{array}{lcll}
\mathit{up}^{\#}(T)(s)(t) & = & \mathtt{false} & \text{if } t = \min(T) \\
\mathit{up}^{\#}(T)(s)(t^{\bullet}) & = & \mathtt{true} & \text{if } (s({}^{\bullet}t) \leq 0) \wedge (s(t) > 0) \text{ and } (t \in T) \\
\mathit{up}^{\#}(T)(s)(t^{\bullet}) & = & \mathtt{false} & \text{otherwise}
\end{array}
$$

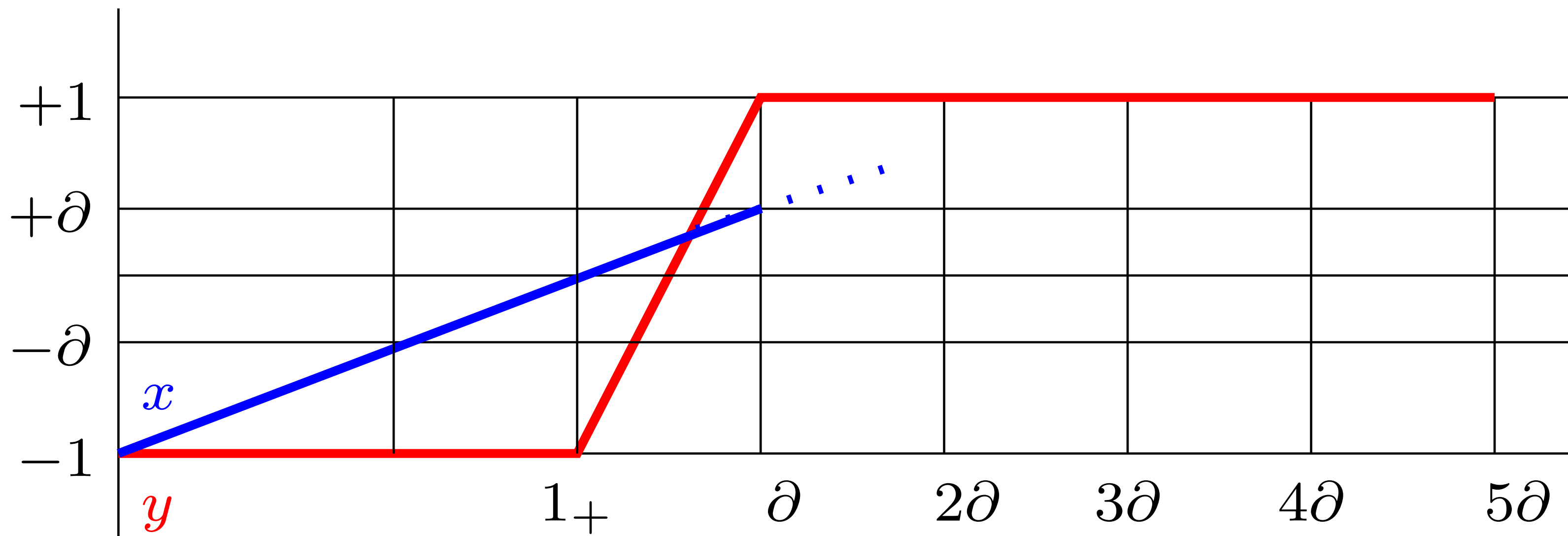
Discrete Operators

T must be a discrete clock, that is: $\mathtt{D}(T)$.

$$
\begin{array}{lcll}
\mathit{pre}^{\#}(T)(s)(v)(t) & = & v & \text{for } t = \min(T) \\
\mathit{pre}^{\#}(T)(s)(v)(t) & = & s({}^{\bullet}t) & \text{otherwise and for all } t \in T
\end{array}
$$

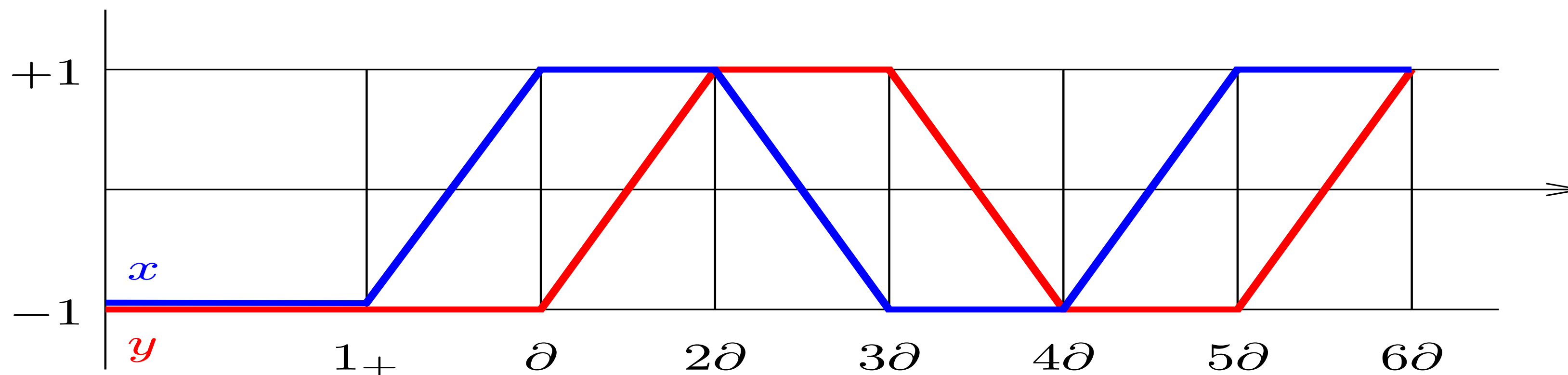
Example 1: reset an integrator on a zero-crossing event

```
let hybrid main () =
  let rec der x = 1.0 init -1.0
      and der y = 0.0 init 1.0 reset -1.0 every up(x) in
  (x,y)
```



Example 2: Unbounded cascades of zero-crossing

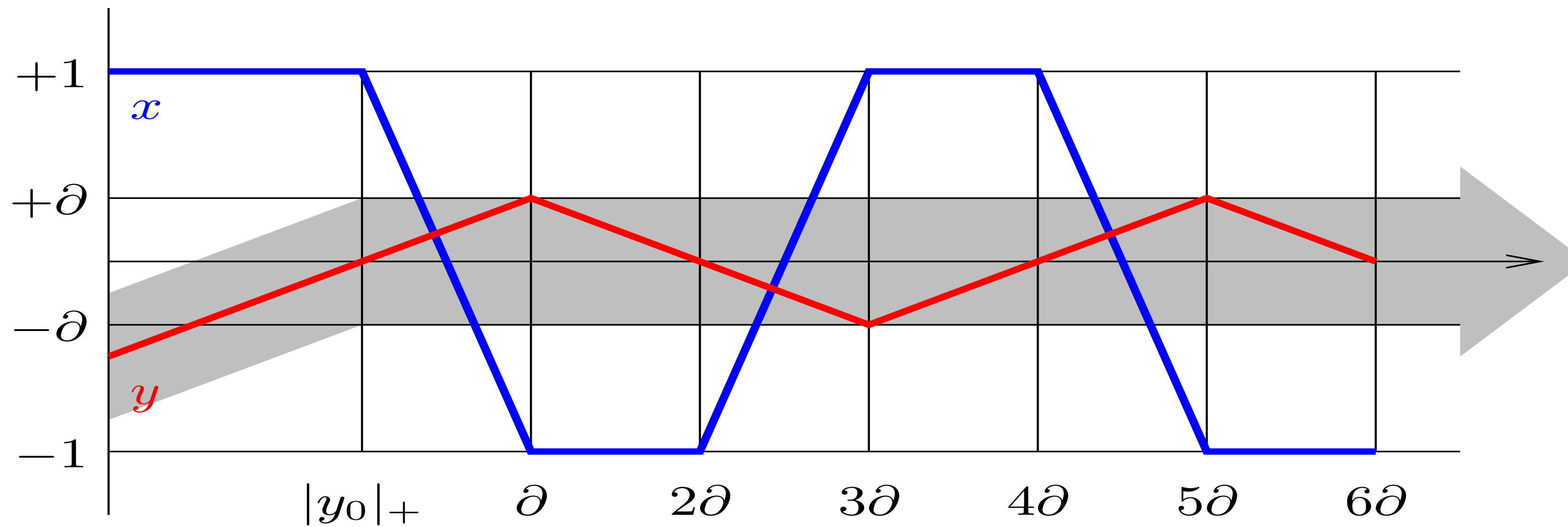
```
let hybrid main () =
  let rec der x = 0.0 init -1.0
              reset -. 1.0 every up(y) | 1.0 every up(-. y) | 1.0 every up(z)
  and der y = 0.0 init -1.0
                reset 1.0 every up(x) | -1.0 every up(-. x)
  and der z = 1.0 init -1.0 in
  (x,y,z)
```



- ∂ represent a "very small" step size in that finitely many ∂'s sum up to ≈ 0.
- At $t = 1$, x and y starts an infinite cascade of zero-crossing while time remains blocked. **This is certainly pathological**.

Example 3: Sliding mode control

```
let hybrid main (y0) =
  let rec der x = 0.0 init -. sgn(y0) reset -1.0 every up(y)
                                           | 1.0 every up(-. y)
  and der y = x init y0 in
  y
```



- Suppose that $y0 < 0.0$. Thus $x_0 > 0$.
- y increases at constant speed until its first zero-crossing, just after $t = |y_0|$.
- Then, y chatters infinitesimally around 0 as its speed alternate between -1 and $+1$ with infinitesimal step ∂.
- **This example is not pathological**. It is equivalent to:

```
let hybrid main (y0) =
  let rec der y = z init y0
  and der x = 0.0 init -. sgn(y0) reset 0 every up(y) in
  y
```

Unbounded cascades of zero-crossings:

- Time did not progress in example (2) while the very same zero-crossing condition has been taken twice.
- Is-it a run-time error? What about a **causality analysis** which accept programs (1) and (3) but reject program (2)?

Compilation

The non-standard semantics is not operational. It serves as a reference to establish the correctness of the compilation. Two problems to address:

1. The compilation of the discrete part, that is, the synchronous subset of the language.
2. The compilation of the continuous part which is to be linked to a black-box numerical solver.

Principle

Translate the program into an only discrete one. Compile the result with an existing synchronous compiler such that it verifies the following invariant:

> **The discrete state, i.e., the values of delays, does not change when all of the zero-crossing conditions are false.**

Said differently: when those conditions are false, the function is combinatorial.

Example (counter)

Add extra input and outputs.

- `up`(e) becomes a fresh boolean input z and generate an equation $up_z = e$.
- `der`$(x) = e$ `init` e_0 `reset` h becomes $dx = e$ and $x = h$ `init` e_0 `default` lx.
- A continuous state variable `last`(x) becomes an input lx.

```
let node counter_ten([z], [ltime], (top, tick)) = (o,[upz],[time],[dtime])
 where
    dtime = 1.0 and time = default ltime init 0.0 reset 0.0 every z
 and o = counter(top, tick) every z init 0
 and upz = time -. 1.0
```

In practice, represent these extra inputs with arrays.

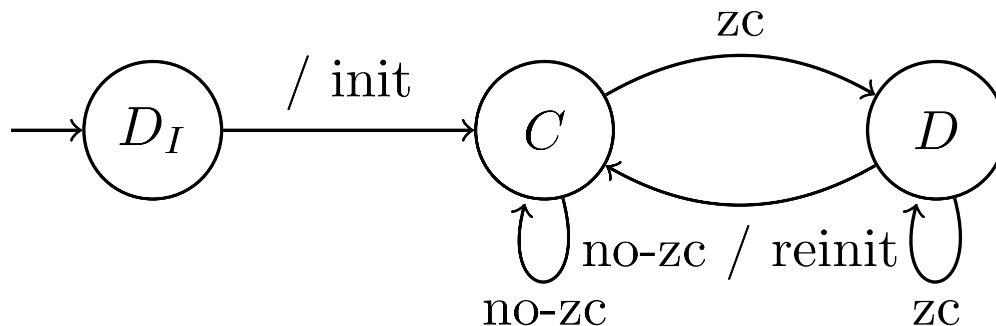
Now, ignoring details of syntax, the function `counter_ten` can be processed by any synchronous compiler, and the generated transition function verifies the invariant.

Interfacing with a numerical solver

We used the Sundials CVODE library. An Ocaml interface has been developed.

Structure of the execution: Run the transition function with two modes, a continuous one and a discrete one

- **Continuous phase:** processed by the numerical solver which stops when a zero-crossing event has been detected.
- **Discrete phase:** compute the consequence of (one or several) zero-crossing(s).



Delta-delayed synchrony *vs* Instantaneous synchrony

For cascaded zero-crossing, two interpretations of $\texttt{up}(e)$ lead to different results.

- **Delta-delay**: the effect of a zero-crossing is delayed by one instant.

$$T \ \texttt{on} \ \texttt{up}(s) = \{t^\bullet \mid (t \in T) \wedge (s({}^\bullet t) \leq 0) \wedge (s(t) > 0)\}$$

- **Instantaneous**: the effect is immediate.

$$T \ \texttt{on} \ \texttt{up}(s) = \{t \mid (t \in T) \wedge (s({}^\bullet t) \leq 0) \wedge (s(t) > 0)\}$$

We have considered the two solutions.

- The first one is simpler to compile. But the discrete state can last several micro-instants.
- The second one is (a little) more complicated to compile.

Simultaneous events A zero-crossing is a boolean signal; they are treated with a priority. Exactly what Simulink does.

Conclusion

Proposal

- To mix signals on discrete time and signal on continuous time.
- A Lustre-like language to combine stream equations with ODE.
- Divide with a type-system, recycle a existing compiler to use a numerical solver as a black-box.

Extension

- (Hybrid/discrete) hierarchical automata can be translated into the basic language
- Combination of solvers.

References

[BBCP11] Albert Benveniste, Timothy Bourke, Benoit Caillaud, and Marc Pouzet. Divide and recycle: types and compilation for a hybrid synchronous language. In *ACM SIGPLAN/SIGBED Conference on Languages, Compilers, Tools and Theory for Embedded Systems (LCTES'11)*, Chicago, USA, April 2011.

[BCP10] Albert Benveniste, Benoit Caillaud, and Marc Pouzet. The Fundamentals of Hybrid Systems Modelers. In *49th IEEE International Conference on Decision and Control (CDC)*, Atlanta, Georgia, USA, December 15-17 2010.

[CBPSV04] Luca Carloni, Maria D. Di Benedetto, Alessandro Pinto, and Alberto Sangiovanni-Vincentelli. Modeling Techniques, Programming Languages, Design Toolsets and Interchange Formats for Hybrid Systems. Technical report, IST-2001-38314 WPHS, Columbus Project, March 19 2004.