



Jet Propulsion Laboratory
California Institute of Technology

Formal Methods Efforts at JPL

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Current Work at JPL

- Scala for modeling
- Runtime verification with BDDs
- Event stream abstraction

Scala for Modeling

Using Scala for development of Hierarchical State Machines

With Rajeev Joshi

class Plant

instance variables

```
alarms : set of Alarm;  
schedule : map Period to set of Expert;  
inv PlantInv(alarms,schedule);
```

operations

```
PlantInv: set of Alarm * map Period to set of Expert ==>  
bool
```

```
PlantInv(as,sch) ==  
return  
(forall p in set dom sch & sch(p) <> {}) and  
(forall a in set as &  
forall p in set dom sch &  
exists expert in set sch(p) &  
a.GetReqQuali() in set expert.GetQuali());
```

types

```
public Period = token;
```

operations

```
public ExpertToPage: Alarm * Period ==> Expert
```

```
ExpertToPage(a, p) ==  
let expert in set schedule(p) be st  
a.GetReqQuali() in set expert.GetQuali()
```

```
in  
return expert
```

```
pre a in set alarms and  
p in set dom schedule
```

```
post let expert = RESULT
```

```
in  
expert in set schedule(p) and  
a.GetReqQuali() in set expert.GetQuali();
```

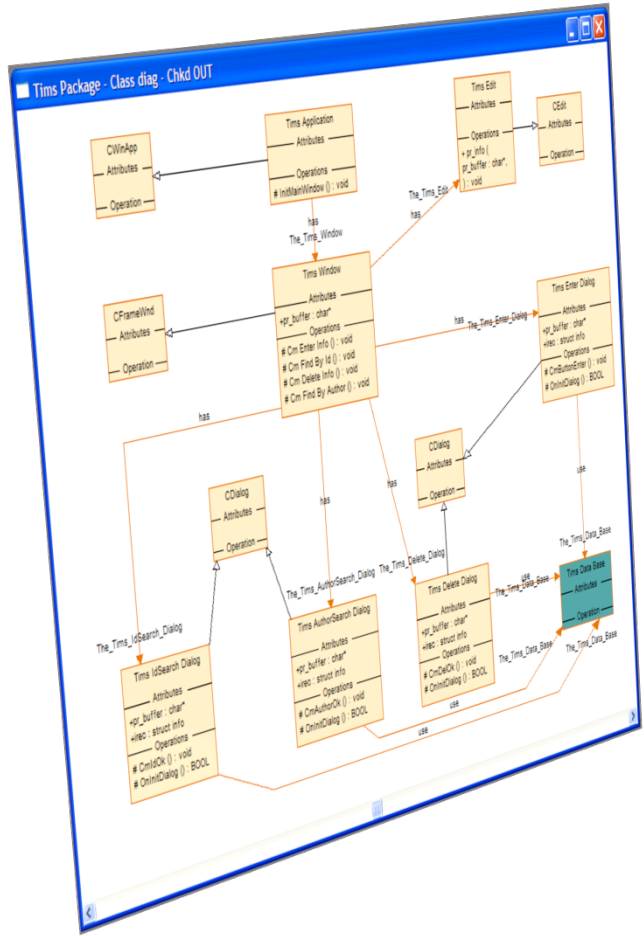
VDM++

Chemical Plant Alarm System

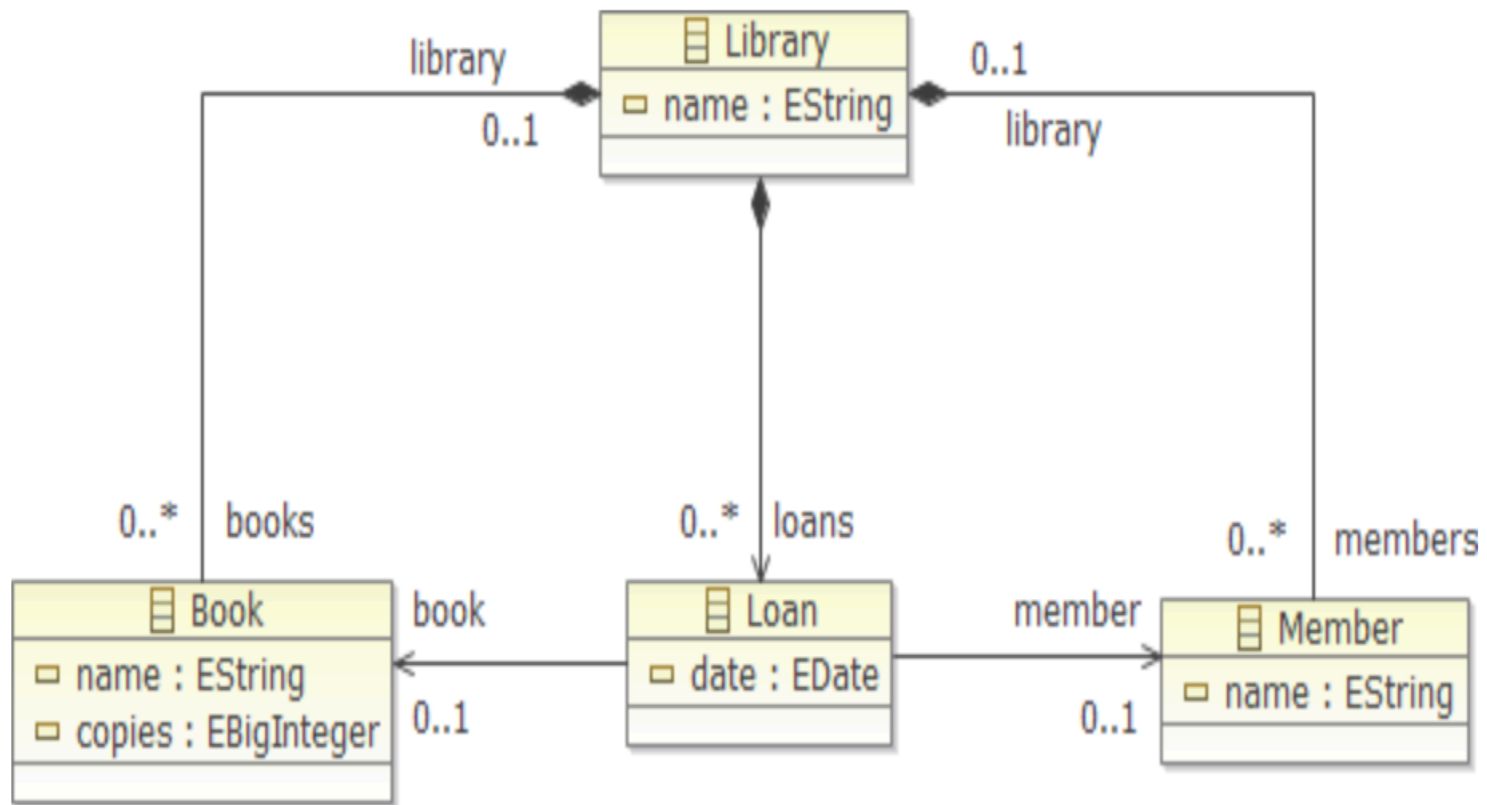
Scala

```
class Plant(alarms: Set[Alarm],  
            schedule: Map[Period, Set[Expert]]) {  
  assert(PlantInv(alarms, schedule))  
  
  def PlantInv(alarms: Set[Alarm], schedule: Map[Period,  
    Set[Expert]]): Boolean =  
    (schedule.keySet forall { schedule(_) != Set() }) &&  
    (alarms forall { a =>  
      schedule.keySet forall { p =>  
        schedule(p) exists { expert =>  
          a.reqQuali ? expert.quali  
        }  
      }  
    })  
  
  def ExpertToPage(a: Alarm, p: Period): Expert = {  
    require(a ? alarms && p ? schedule.keySet)  
    schedule(p) suchthat {expert =>  
      a.reqQuali ? expert.quali}  
  } ensuring { expert =>  
    a.reqQuali ? expert.quali &&  
    expert ? schedule(p)  
  }  
}
```

Modeling @ JPL



SysML



Requirement

The number of loans that a book is part of should be less than or equal to the number of copies of the book

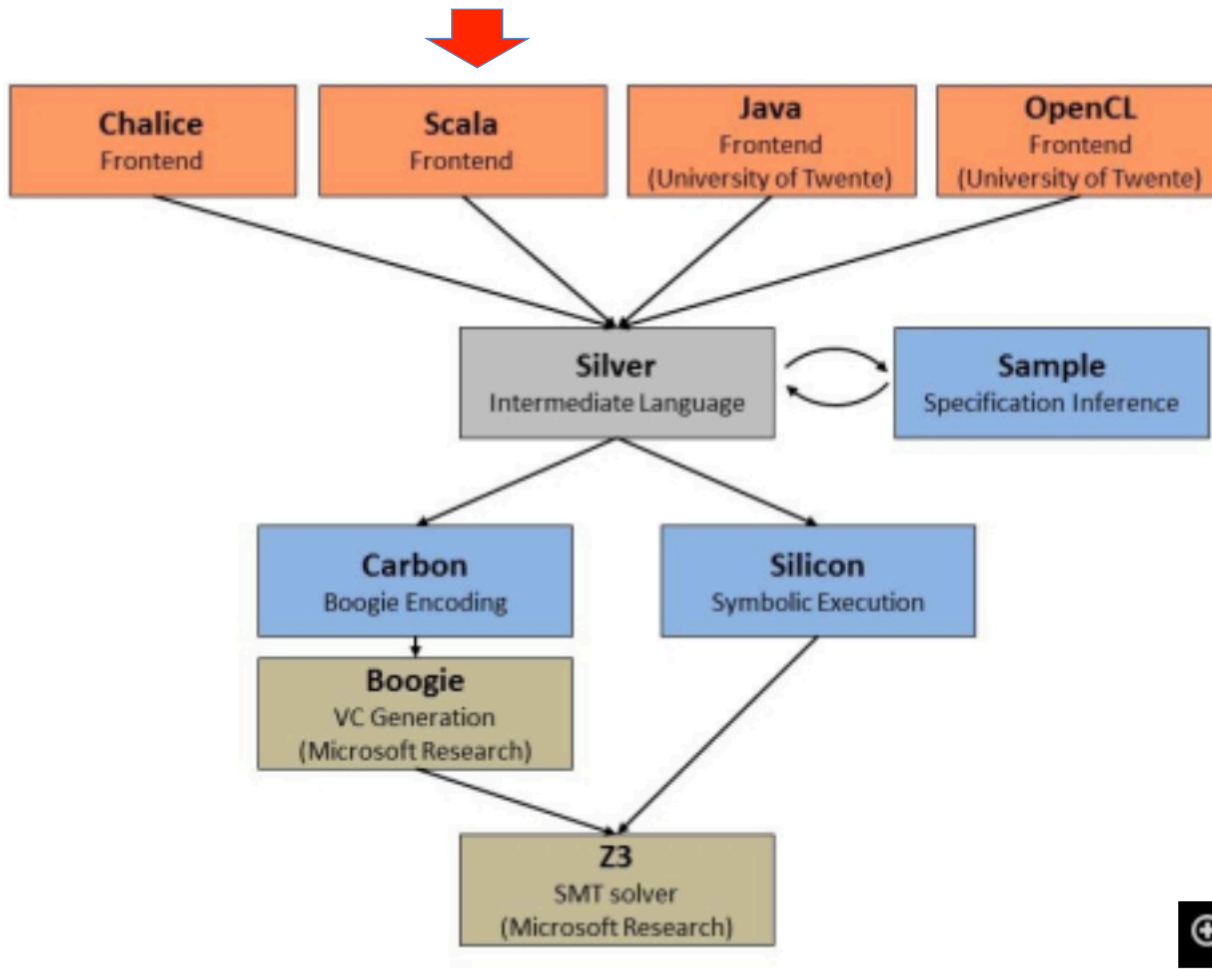
K

```
class Book {  
  name : String  
  copies : Int  
  library : Library  
  
  loans : Set[Loan] =  
    Set{ loan | loan : Loan •  
      loan ∈ library .loans ∧ loan.book = this  
    }  
  
  fun isAvailable : Bool {  
    size(loans) < copies  
  }  
  
  req CopiesPositive : copies > 0  
  req SufficientCopies : size(loans) ≤ copies  
}
```

Scala

```
trait Book extends Model {  
  var name: String  
  var copies: Int  
  var library : Library  
  
  def loans: Set[Loan] =  
    library .loans. filter ( _.book eq this)  
  
  def isAvailable (): Boolean = loans.size < copies  
  
  invariant ("CopiesPositive") { copies > 0 }  
  
  invariant ("SufficientCopies") {  
    loans.size <= copies  
  }  
}
```

Formal Verification of Scala Programs



Peter Muller and Alex Summers

Motivation for focusing on Hierarchical State Machines

- **HSMs used extensively** in FSW (on MSL, SMAP, M2020, FswCore)
- FSW typically has **many HSMs interacting** with each other and with devices
- During design, **hard to analyze** and understand complex interactions among HSMs, and how behavior is affected
 - by faults
 - by devices in off-nominal fashion (e.g., taking longer to warm up)
- **We need** a way to perform easy design and analysis of HSMs:
 - easy to **model** existing MSL/SMAP designs in expressive DSL
 - easy to write scenario **test cases**
 - easy to write properties to be **monitored** during tests
 - eventually **verify**

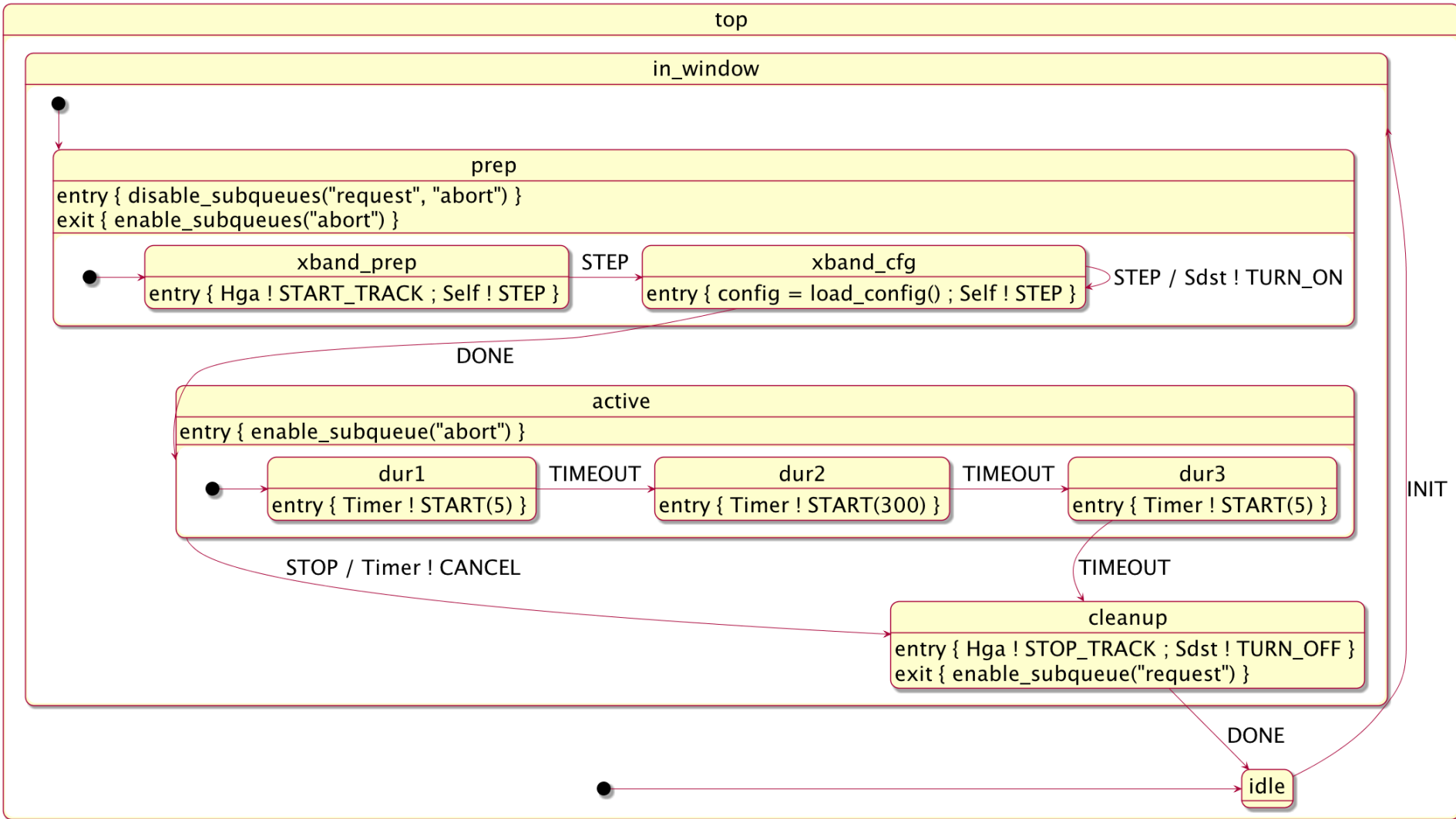
Approach

- Develop domain-specific language as an API (internal DSL) in the high-level strongly typed object-oriented and functional programming language **Scala**.
- This allows fast and safe **prototyping** of new functionality.
- Design and implement DSL for **Hierarchical State Machines** (HSMs) in Scala.
- Design and implement DSL for **monitoring** and testing in Scala.
- Develop **automated testing** framework.
- Develop **formal verification** technology for Scala.

Case Study: CBM (Coordinated Communications Behaviors), used on MSL/M2020/Europa Clipper

- CBM HSM **inherited from MSL** has **~50 states**.
- **Interacts with many devices** and low-level subsystems in order to establish spacecraft state for telecom configuration and for communication windows.
- Complex interactions have led to **surprises during MSL**
 - Sol 696-698 anomaly resulted in 3 days of lost science due to frequency sweep not happening on time due to prolonged mounting of file system.
- Ideally, these **should have been discovered during V&V**
 - but hard to enumerate all interactions using traditional testing.
- **Our approach** should allow **automatic test-case generation** from high-level HSM and scenario descriptions; during each test case run, a set of **monitored properties** are checked automatically.

A Hierarchical State Machine



```

1  case object STEP extends CbmMessage("transition")
2  case object DONE extends CbmMessage("transition")
3  case object TIMEOUT extends CbmMessage("transition")
4  case object STOP extends CbmMessage("abort")
5  case object INIT extends CbmMessage("request")
6
7  class CbmHsm extends MslHsm {
8      object top extends state() {}
9      object idle extends state(top, true) {
10         when {
11             case INIT => in_window
12         }
13     }
14     object in_window extends state(top) {}
15     object prep extends state(in_window, true) {
16         entry { disable_subqueues("request", "abort") }
17         exit { enable_subqueues("abort") }
18     }
19     object xband_prep extends state(prepare, true) {
20         entry { Hga ! START_TRACK ; Self ! STEP }
21         when { case STEP => xband_cfg }
22     }
23     object xband_cfg extends state(prepare) {
24         entry { config = load_config() ; Self ! STEP }
25         when {
26             case STEP => stay exec { Sdst ! TURN_ON }
27             case DONE => active
28         }
29     }
30     object active extends state(in_window) {
31         entry { enable_subqueues("abort") }
32         when { case STOP => cleanup exec { Timer ! CANCEL } }
33     }
34     ...
35 }

```



```
1 // In state in_window, the request subqueue is disabled
2 class QueueCheck extends MSLMonitor {
3   invariant ("requestDisabled") {
4     Cbm.inState("in_window") ==> !Cbm.isEnabled("request")
5   }
6 }
7
8 // A Timer is never started while a previous timer is active
9 class TimerCheck extends MSLMonitor {
10  always {
11    case TIM_EVR_STARTED(-) => watch {
12      case TIM_EVR_FIRED(-) | TIM_EVR_CANCELED(-) => ok
13      case TIM_EVR_STARTED(-) => error("Timer restarted")
14    }
15  }
16 }
```

Plans for Test Case Generation

```
class scenario1 extends TestScenario {  
  at(30) exec { Cbm ! INIT }  
}
```

```
class scenario2 extends scenario1 {  
  Cbm in "dur2" exec { Cbm ! STOP }  
}
```

```
1 taskSet := set of tasks in scenario  
2 while (taskSet ≠ ∅) {  
3   choose T in taskSet with least value of T.time such that T is enabled  
4   advance current time to T.time  
5   perform T.action  
6   while (there is an HSM that has a pending message) {  
7     choose HSM H such that H has a pending message  
8     execute H on its highest priority message  
9   }  
10 }
```

HSMs in Scala

```
trait HSM[Event] {...}
```

The HSM trait defines the following types and values used throughout:

```
type Code = Unit ⇒ Unit
```

```
type Target = (state, Code)
```

```
type Transitions = PartialFunction[Event, Target]
```

```
val noTransitions: Transitions = {case _ if false ⇒ null }
```

```
val skip: Code = (x: Unit) ⇒ {}
```

HSMs in Scala

```
case class state(parent: state = null, init : Boolean = false) {  
  var entryCode: Unit ⇒ Unit = skip  
  var exitCode: Unit ⇒ Unit = skip  
  var transitions : Transitions = noTransitions  
  ...  
  def entry(code: ⇒ Unit): Unit = {entryCode = (x: Unit) ⇒ code}  
  def exit(code: ⇒ Unit): Unit = {exitCode = (x: Unit) ⇒ code}  
  def when(ts: Transitions ): Unit = {transitions = ts}  
  
  implicit def state2Target(s: state ): Target = (s, skip)  
  implicit def state2Exec(s: state) = new {  
    def exec(code: ⇒ Unit) = (s, (x: Unit) ⇒ code) }  
}
```

HSMs in Scala

```
def initial (s: state): Unit = {current = s.getInnerMostState}
```

```
var initialState : state = null
```

```
if (parent != null && init) {parent. initialState = this}
```

```
def getInnerMostState: state =
```

```
  if ( initialState == null) this else initialState .getInnerMostState
```

```
def getSuperStates: List [state] =
```

```
  (if (parent == null) Nil else parent.getSuperStates) ++List(this)
```

HSMs in Scala

```
var current: state = null
```

```
def submit(event: Event): Unit = {  
  findTriggerHappyState(current, event) match {  
    case None ⇒  
    case Some(triggerState) ⇒  
      val ( transitionState , transitionCode ) = triggerState . transitions (event)  
      val targetState = transitionState . getInnerMostState  
      val ( exitStates , enterStates ) = getExitEnter((current, targetState))  
      for (s ← exitStates) s.exitCode()  
      transitionCode()  
      for (s ← enterStates) s.entryCode()  
      current = targetState  
  }  
}
```

HSMs in Scala

```
def findTriggerHappyState(s: state, event: Event): Option[state] =  
  if (s.transitions.isDefinedAt(event)) Some(s) else  
  if (s.parent == null) None else findTriggerHappyState(s.parent, event)
```

Monitors in Scala

```
1  class Monitor[Event] {  
2    type Transitions = PartialFunction [Event, Set[state]]  
3    var states: Set[state] = Set()  
4    var invariants: List [(String, Unit => Boolean)] = Nil  
5    ...  
6    trait state { thisState =>  
7      var transitions: Transitions = noTransitions  
8      ...  
9      def apply(event: Event): Option[Set[state]] =  
10     if ( transitions .isDefinedAt(event)) Some(transitions(event)) else None  
11     def watch(ts: Transitions) { transitions = ts}  
12     def always(ts: Transitions) { transitions = ts andThen (- + this)}  
13   }  
14   def watch(ts: Transitions) = new state { watch(ts) }  
15   def always(ts: Transitions) = new state { always(ts) }  
16 }
```


Publications

[1] Modeling and Monitoring of Hierarchical State Machines in Scala

K. Havelund and R. Joshi. Submitted March 2017.

[2] Modeling Rover Communication using Hierarchical State Machines in Scala

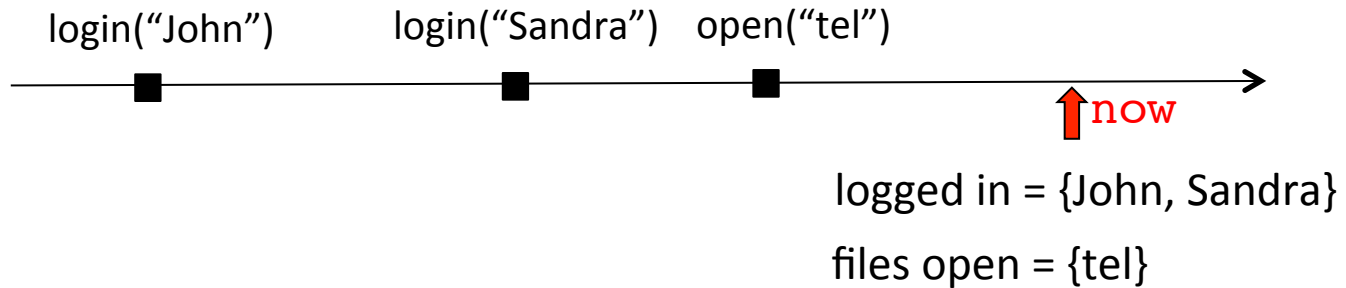
K. Havelund and R. Joshi. Submitting May 5, 2017.

QTL

Quantified temporal logic for monitoring using BDDs

With Doron Peled (Bar Ilan University, Israel)

The Problem



Property:

Whenever a user accesses a file:

- *the user must have logged in*
- *the file must have been opened*

The Logic

$$\begin{aligned} \varphi &::= \text{true} \mid p(t_1, \dots, t_n) \mid \neg\varphi \mid \varphi_1 \vee \varphi_2 \mid \exists x \bullet \varphi \mid \ominus\varphi \mid \varphi_1 \mathbf{S} \varphi_2 \\ t &::= c \mid x \end{aligned}$$

Derived Constructs

$$\text{false} = \neg\text{true}$$
$$\varphi_1 \wedge \varphi_2 = \neg(\neg\varphi_1 \vee \neg\varphi_2)$$
$$\varphi_1 \Rightarrow \varphi_2 = \neg\varphi_1 \vee \varphi_2$$
$$\forall x \bullet \varphi = \neg\exists x \bullet \neg\varphi$$
$$\mathbf{P} \varphi = \text{true} \mathbf{S} \varphi$$
$$\mathbf{H} \varphi = \neg\mathbf{P} \neg\varphi$$
$$[\varphi_1, \varphi_2) = (\neg\varphi_2) \mathbf{S} \varphi_1$$

Example

$$\forall \text{user} \bullet \forall \text{file} \bullet$$
$$\text{access}(\text{user}, \text{file}) \Rightarrow$$
$$[\text{login}(\text{user}), \text{logout}(\text{user}))$$
$$\wedge$$
$$[\text{open}(\text{file}), \text{close}(\text{file}))$$

First Semantics

- $(\varepsilon, \sigma, i) \models \text{true}$.
- $(\varepsilon, \sigma, i) \models p(a)$ iff $p(a) \in \sigma[i]$.
- $([v \mapsto a], \sigma, i) \models p(v)$ if $p(a) \in \sigma[i]$.
- $(\gamma, \sigma, i) \models (\varphi \wedge \psi)$ if $(\gamma|_{\text{vars}(\varphi)}, \sigma, i) \models \varphi$ and $(\gamma|_{\text{vars}(\psi)}, \sigma, i) \models \psi$.
- $(\gamma, \sigma, i) \models \neg\varphi$ if not $(\gamma, \sigma, i) \models \varphi$.
- $(\gamma, \sigma, i) \models (\varphi \mathcal{S} \psi)$ if for some $j \leq i$, $(\gamma|_{\text{vars}(\psi)}, \sigma, j) \models \psi$ and for all $j < k \leq i$ $(\gamma|_{\text{vars}(\varphi)}, \sigma, k) \models \varphi$.
- $(\gamma, \sigma, i) \models \ominus\varphi$ if $i > 1$ and $(\gamma, \sigma, i-1) \models \varphi$.
- $(\gamma, \sigma, i) \models \exists x \varphi$ if there exists $a \in \text{domain}(x)$ such that⁵ $(\gamma[x \mapsto a], \sigma, i) \models \varphi$.

$$(\varphi \mathcal{S} \psi) = (\psi \vee (\varphi \wedge \ominus\varphi \mathcal{S} \psi))$$

- $(\gamma, \sigma, i) \models (\varphi \mathcal{S} \psi)$ if $(\gamma|_{\text{vars}(\psi)}, \sigma, i) \models \psi$ or both $(\gamma|_{\text{vars}(\varphi)}, \sigma, i) \models \varphi$ and $(\gamma, \sigma, i-1) \models (\varphi \mathcal{S} \psi)$.

Second Semantics

- $I[\varphi, \sigma, 0] = \emptyset$.
- $I[\text{true}, \sigma, i] = \{\varepsilon\}$.
- $I[p(a), \sigma, i] = \text{if } p(a) \in \sigma[i] \text{ then } \varepsilon, \text{ else } \emptyset$.
- $I[p(v), \sigma, i] = \{[v \mapsto a] \mid p(a) \in \sigma[i]\}$.
- $I[(\varphi \wedge \psi), \sigma, i] = I[\varphi, \sigma, i] \cap I[\psi, \sigma, i]$.
- $I[\neg\varphi, \sigma, i] = A_{\text{vars}(\varphi)} \setminus I[\varphi, \sigma, i]$.
- $I[(\varphi \mathcal{S} \psi), \sigma, i] = I[\psi, \sigma, i] \cup (I[\varphi, \sigma, i] \cap I[(\varphi \mathcal{S} \psi), \sigma, i - 1])$.
- $I[\ominus\varphi, \sigma, i] = I[\varphi, \sigma, i - 1]$.
- $I[\exists x \varphi, \sigma, i] = \text{proj}(I[\varphi, \sigma, i], \{x\})$.

Mapping data to BDDs

prop p: **forall** f . close(f) \rightarrow **exists** m . **P** open(f,m)

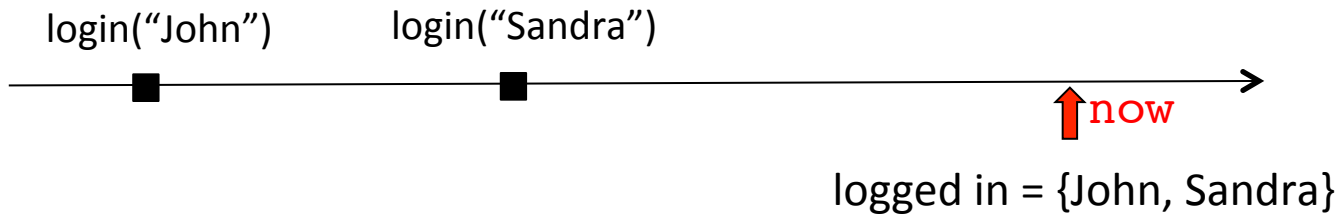
$\langle open(input, read), open(output, write), close(out) \rangle$

$f \mapsto [input \mapsto 000, output \mapsto 001, out \mapsto 010],$
 $m \mapsto [read \mapsto 000, write \mapsto 001]$

Algorithm

1. Initially, for each subformula φ , $\text{now}(\varphi) = 0$.
2. Observe a new state (as set of ground predicates) s as input.
3. Let $\text{pre} := \text{now}$.
4. Make the following updates for each subformula. If φ is a subformula of ψ then $\text{now}(\varphi)$ is updated before $\text{now}(\psi)$.
 - $\text{now}(\text{false}) = 0$
 - $\text{now}(p(a)) = \text{if } p(a) \in s \text{ then } 1 \text{ else } 0$
 - $\text{now}(p(x)) = \text{if } p(a) \in s \text{ then } \mathbf{build}(x, a) \text{ else } 0$
 - $\text{now}((\varphi \wedge \psi)) = \mathbf{and}(\text{now}(\varphi), \text{now}(\psi))$
 - $\text{now}(\neg\varphi) = \mathbf{not}(\text{now}(\varphi))$
 - $\text{now}((\varphi \mathcal{S} \psi)) = \mathbf{or}(\text{now}(\psi), \mathbf{and}(\text{now}(\varphi), \text{pre}((\varphi \mathcal{S} \psi))))$.
 - $\text{now}(\ominus \varphi) = \text{pre}(\varphi)$
 - $\text{now}(\exists x \varphi) = \mathbf{exists}(\langle x_0, \dots, x_n \rangle, \text{now}(\varphi))$
5. Goto step 2.

The Implementation Binary Decision Diagrams



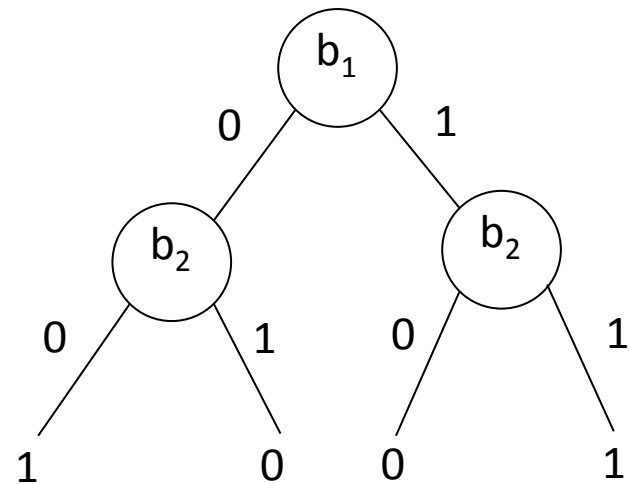
Users as bit patterns

	$b_1 b_2$
John	→ 00
Ann	→ 01
Mike	→ 10
Sandra	→ 11

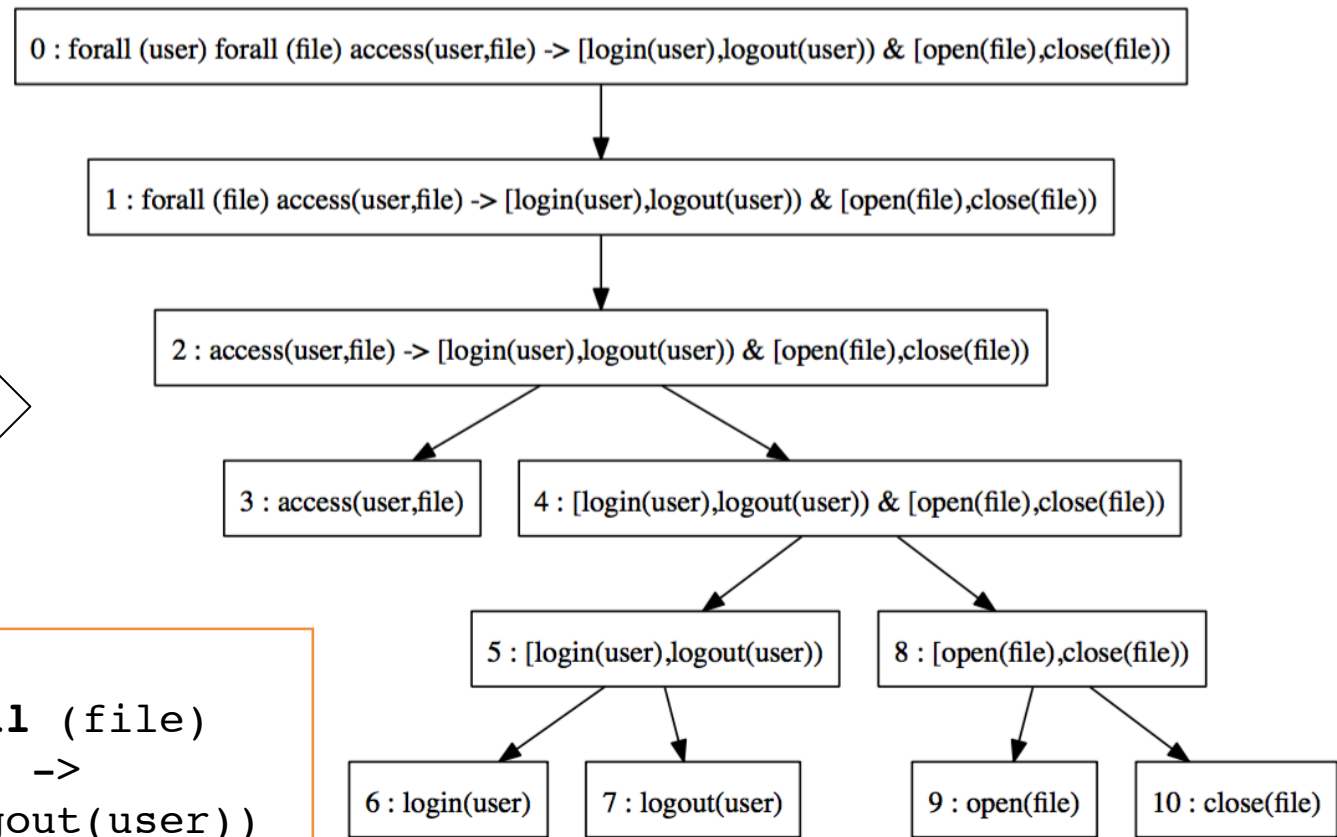
The set
{John, Sandra}
as a Boolean
expression:

$$(b_1 = 0 \wedge b_2 = 0) \vee (b_1 = 1 \wedge b_2 = 1)$$

Boolean expression
as BDD:



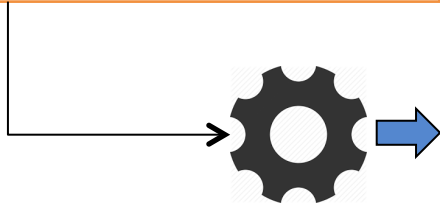
The Structure of a Property



```
prop secure :  
  forall (user) forall (file)  
    access(user,file) ->  
      [login(user),logout(user)]  
      &  
      [open(file),close(file)]
```

Example Application

```
prop secure :  
  forall (user) forall (file)  
    access(user,file) ->  
      [login(user),logout(user)]  
      &  
      [open(file),close(file)]
```



```
login,John  
open,tel  
access,John,tel  
close,tel  
access,John,tel  
logout,John
```

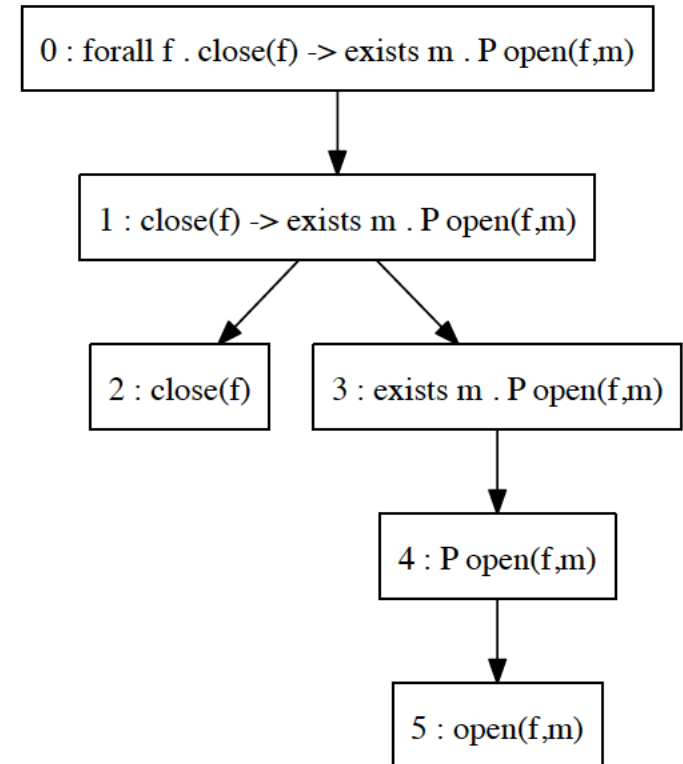
```
class Formula_secure extends Formula {  
  val var_user :: var_file :: Nil = declareVariables("user", "file")  
  
  override def evaluate(): Boolean = {  
    now(10) = build("close")(V("file"))  
    now(9) = build("open")(V("file"))  
    now(8) = now(9).or(now(10).not().and(pre(8)))  
    now(7) = build("logout")(V("user"))  
    now(6) = build("login")(V("user"))  
    now(5) = now(6).or(now(7).not().and(pre(5)))  
    now(4) = now(5).and(now(8))  
    now(3) = build("access")(V("user"),V("file"))  
    now(2) = now(3).not().or(now(4))  
    now(1) = now(2).forall(var_file)  
    now(0) = now(1).forall(var_user)  
  
    tmp = now  
    now = pre  
    pre = tmp  
    !tmp(0).isZero  
  }  
  
  pre = Array.fill(11)(False)  
  now = Array.fill(11)(False)  
}
```



*** Property secure violated on event number 5:
access(John,tel)

prop p: forall f . close(f) → exists m . P open(f,m)

```
class Formula_p extends Formula {  
  var pre: Array[BDD] = Array.fill(6)(False)  
  var now: Array[BDD] = Array.fill(6)(False)  
  var tmp: Array[BDD] = null  
  val var_f :: var_m :: Nil =  
    declareVariables("f", "m")  
  
  override def evaluate(): Boolean = {  
    now(5) = build("open")(V("f"),V("m"))  
    now(4) = now(5).or(pre(4))  
    now(3) = now(4).exist(var_m)  
    now(2) = build("close")(V("f"))  
    now(1) = now(2).not().or(now(3))  
    now(0) = now(1).forall(var_f)  
    tmp = now; now = pre; pre = tmp  
    !tmp(0).isZero  
  }  
}
```



Evaluation of Trace

⟨open(input, read), open(output, write), close(out)⟩

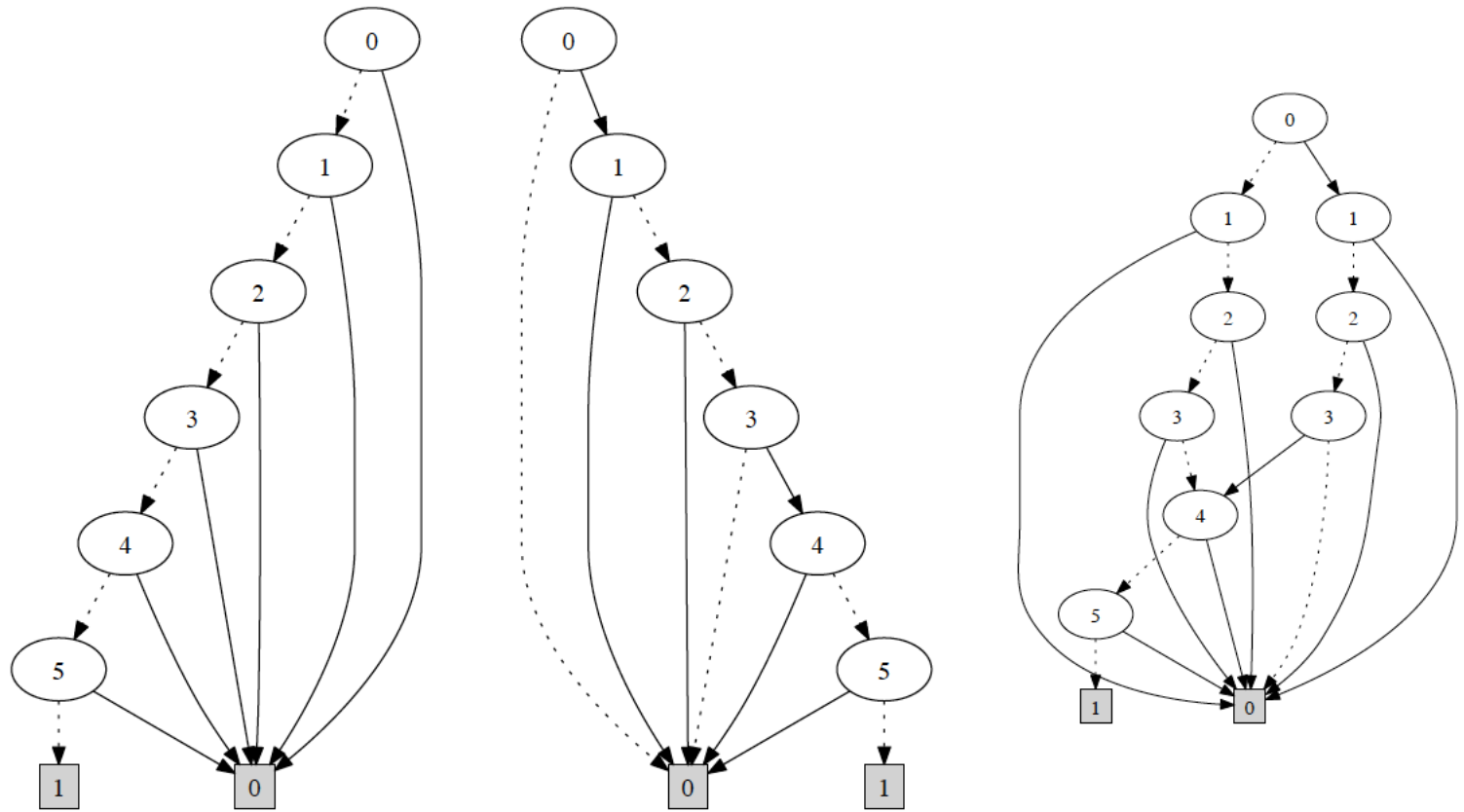


Fig.2: BDD for nodes for (left) subformula 5 on first event, (mid) subformula 5 on second event, (right) subformula 4 on second event

Evaluation Properties in QTL

prop access : **forall** u . **forall** f .

access(u,f) \rightarrow [login(u),logout(u)] & [open(f),close(f))

prop file : **forall** f .

close(f) \rightarrow **exists** m . @ [open(f,m),close(f))

prop fifo : **forall** x .

(enter(x) \rightarrow ! @ **P** enter(x)) &

(exit(x) \rightarrow ! @ **P** exit(x)) &

(exit(x) \rightarrow @ **P** enter(x)) &

(**forall** y . (exit(y) & **P** (enter(y) & @ **P** enter(x))) \rightarrow @ **P** exit(x))

Evaluation Properties in MonPoly

```
/* access */ FORALL u. (FORALL f.  
  ( access(u,f) IMPLIES  
    (((NOT logout(u)) SINCE login(u)) AND (NOT close(f) SINCE[0,*] open(f))))))
```

```
/* file */ FORALL f .  
  (close(f) IMPLIES (EXISTS m . PREVIOUS ( NOT close(f) SINCE[0,*] open(f,m) )))
```

```
/* fifo */ FORALL x. (  
  (enter(x) IMPLIES NOT PREVIOUS ONCE[0,*] enter(x)) AND  
  (exit (x) IMPLIES NOT PREVIOUS ONCE[0,*] exit(x)) AND  
  (exit (x) IMPLIES PREVIOUS ONCE[0,*] enter(x)) AND  
  FORALL y.  
    (( exit (y) AND ONCE[0,*] (enter(y) AND PREVIOUS ONCE[0,*] enter(x)))  
      IMPLIES PREVIOUS ONCE exit(x)))
```


Evaluation Results

Table 1: Evaluation of QTL and MONPOLY

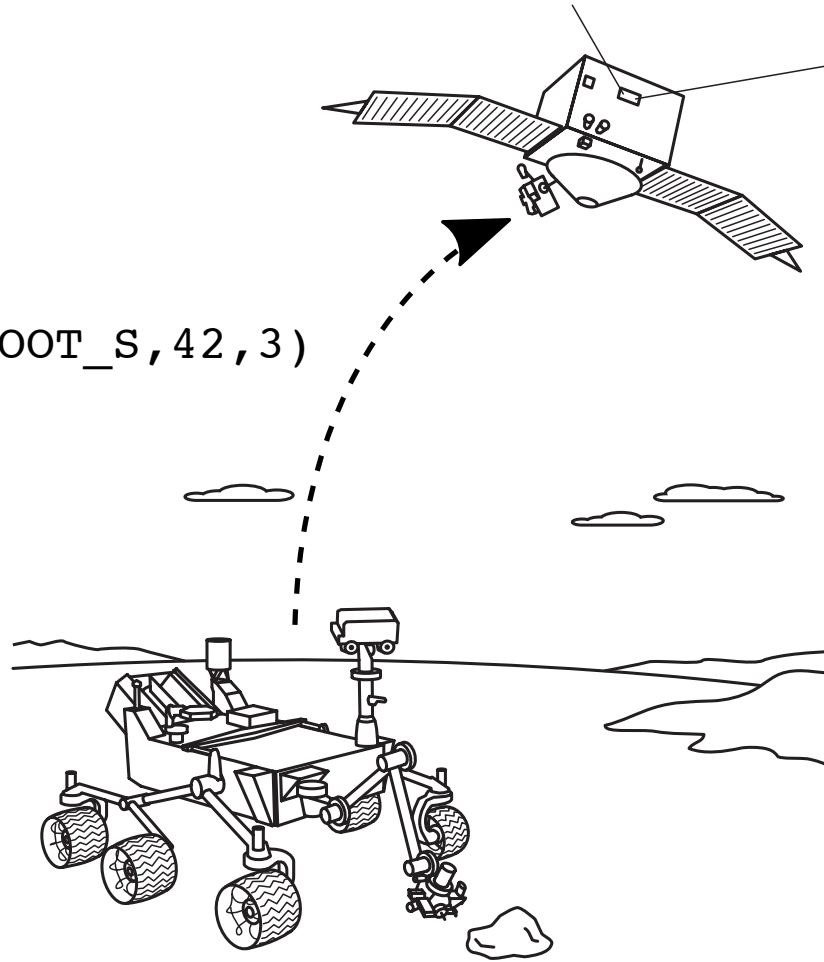
Property	Trace length	MONPOLY (sec)	QTL (sec) bits per var.: 20 (40, 60)
ACCESS	11,006	1.9	3.1 (3.3, 3.2)
	110,006	241.9	6.1 (9.1, 10.9)
	1,100,006	58,455.8	36.8 (61.9, 88.8)
FILE	11,004	61.1	2.8 (2.8, 3.0)
	110,004	7,348.7	6.3 (6.5, 8.6)
	1,100,004	DNF	30.3 (43.9, 59.5)
FIFO	5,051	158.3	195.4 (OOM, ?)
	10,101	1140.0	ERR (?, ?)

Nfer

A tool for event stream
abstraction

With Rajeev Joshi and Sean Kauffman (U. of Waterloo, Canada)

EVR(BOOT_S,42,3)



Current Situation



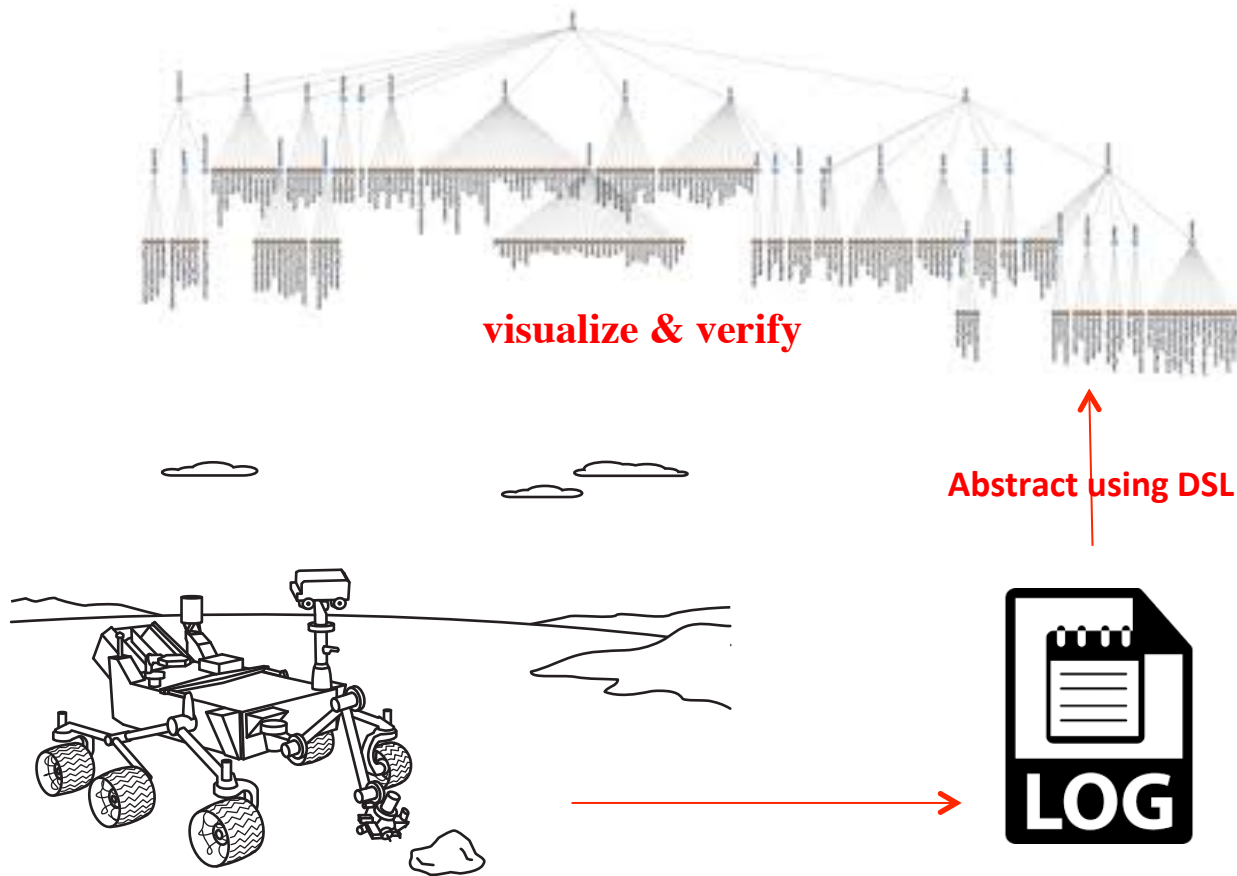
vrf1
.py

tst2
.awk

chk3
.py

Nfer

A Tool for Event Abstraction



Grammar for DSL

```
specification ::= rule+ | module+
module ::= 'module' identifier '{' [imports] rule* '}'
imports ::= 'import' identifier (',' identifier)* ';'
rule ::= identifier ':-' intervalExpression [whereExpression] [mapExpression] [endPoints]
intervalExpression ::= primaryIntervalExpression (intervalOp primaryIntervalExpression)*
primaryIntervalExpression ::= atomicIntervalExpression | parenIntervalExpression
atomicIntervalExpression ::= [label] identifier
parenIntervalExpression ::= '(' intervalExpression ')'
label ::= identifier ':'
whereExpression ::= 'where' expression
mapExpression ::= 'map' '{' identifier '->' expression (',' identifier '->' expression)* '}'
endPoints ::= 'begin' expression 'end' expression
intervalOp ::= 'also' | 'before' | 'meet' | 'during' | 'start' | 'finish' | 'overlap' | 'slice' | 'coincide'
expression ::= comparisonExpression (andorOp comparisonExpression)*
comparisonExpression ::= plusminusExpression (comparisonOp plusminusExpression)*
plusminusExpression ::= muldivExpression (plusminusOp muldivExpression)*
muldivExpression ::= unaryExpression (muldivOp unaryExpression)*
unaryExpression ::=
  stringLiteral | intLiteral | doubleLiteral | booleanLiteral |
  beginTime | endTime | mapField | callExpression |
  '(' expression ')' | unaryOp unaryExpression
mapField ::= identifier '.' identifier
beginTime ::= identifier '.' BEGIN
endTime ::= identifier '.' END
unaryOp ::= '-' | '!'
muldivOp ::= '*' | '/' | '%'
plusminusOp ::= '+' | '-'
comparisonOp ::= '<' | '<=' | '>' | '>=' | '=' | '!='
andorOp ::= '&' | '|'
callExpression ::= 'call' '(' identifier ')'
```

Event Abstraction with nfer

NAME	TIME	PARAMS
DOWNLINK	10	size -> 430

BOOT_S	42	count -> 3
--------	----	------------

TURN_ANTENNA	80	
--------------	----	--

START_RADIO	90	<i>BOOT</i>
-------------	----	-------------

DOWNLINK	100	size -> 420
----------	-----	-------------

BOOT_E	160	
--------	-----	--

DBOOT

RISK

STOP_RADIO	205	
------------	-----	--

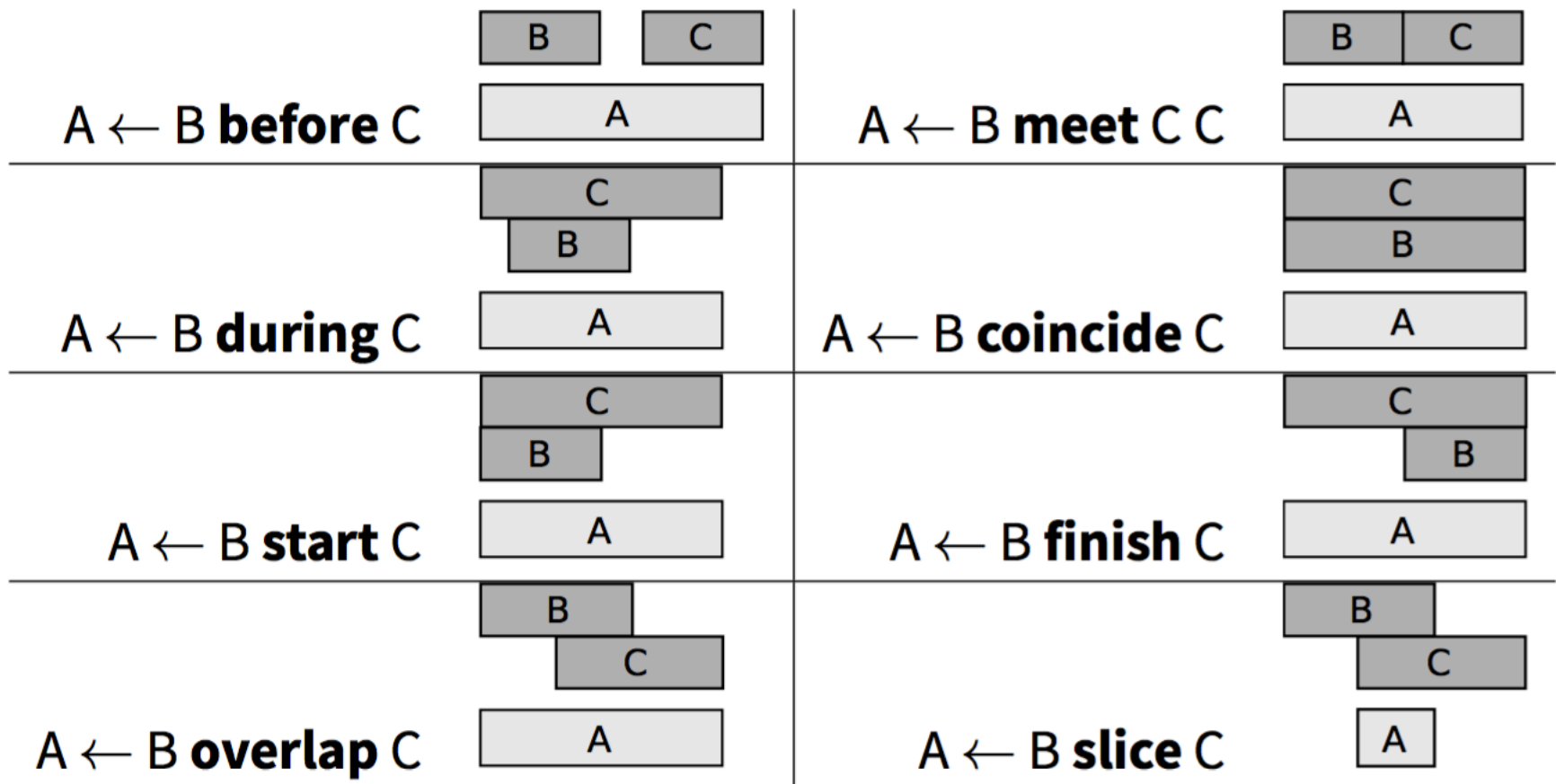
BOOT_S	255	count -> 4
--------	-----	------------

START_RADIO	286	<i>BOOT</i>
-------------	-----	-------------

BOOT_E	312	
--------	-----	--

TURN_ANTENNA	412	
--------------	-----	--

Based on Allen Logic



Specification in new DSL

```
BOOT :- BOOT_S before BOOT_E  
      map {count -> BOOT_S.count}
```

```
DBOOT :- b1:BOOT before b2:BOOT  
        where b2.begin - b1.end <= 300  
        map {count -> b2.count}
```

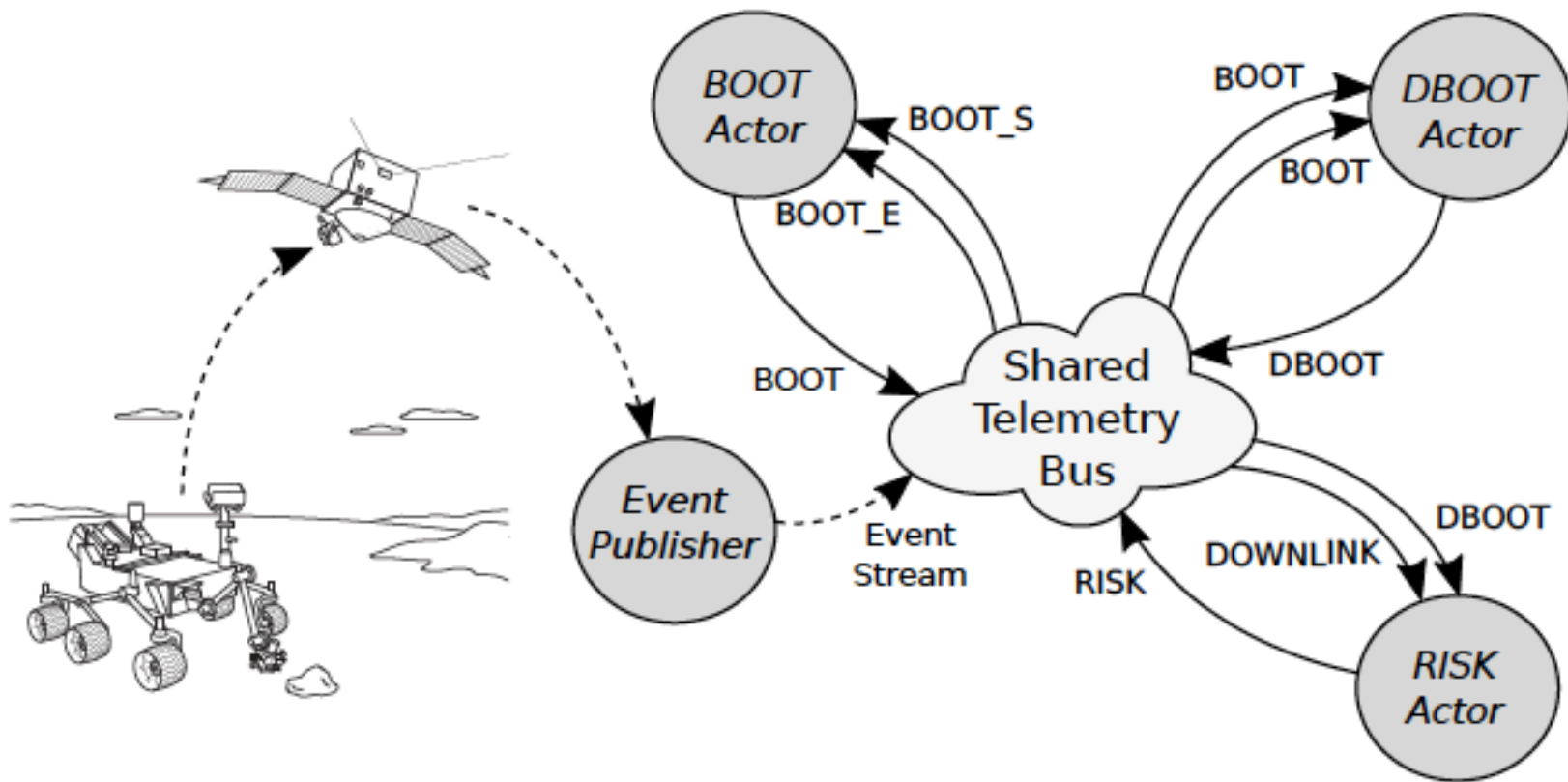
```
RISK :- DOWNLINK during DBOOT  
      map {count -> DBOOT.count}
```

Using modules

```
module Booting {  
  BOOT :- BOOT_S before BOOT_E  
    map {count -> BOOT_S.count}  
}  
  
module DoubleBooting {  
  import Booting;  
  
  DBOOT :- b1:BOOT before b2:BOOT  
    where b2.begin - b1.end <= 300  
    map {count -> b2.count}  
}  
  
module Risking {  
  import DoubleBooting;  
  
  RISK :- DOWNLINK during DBOOT  
    map {count -> DBOOT.count}  
}
```

nfer Scala DSL

```
class DoubleBoot extends Nfer {  
  "BOOT" :- ("BOOT_S" before "BOOT_E" map {  
    case (m1,m2) => Map("count" -> m1("count"))  
  })  
  
  "DBOOT" :- ("BOOT" before "BOOT" within 300 map (_. _2))  
  
  "RISK" :- ("DOWNLINK" during "DBOOT" map (_. _2))  
}
```

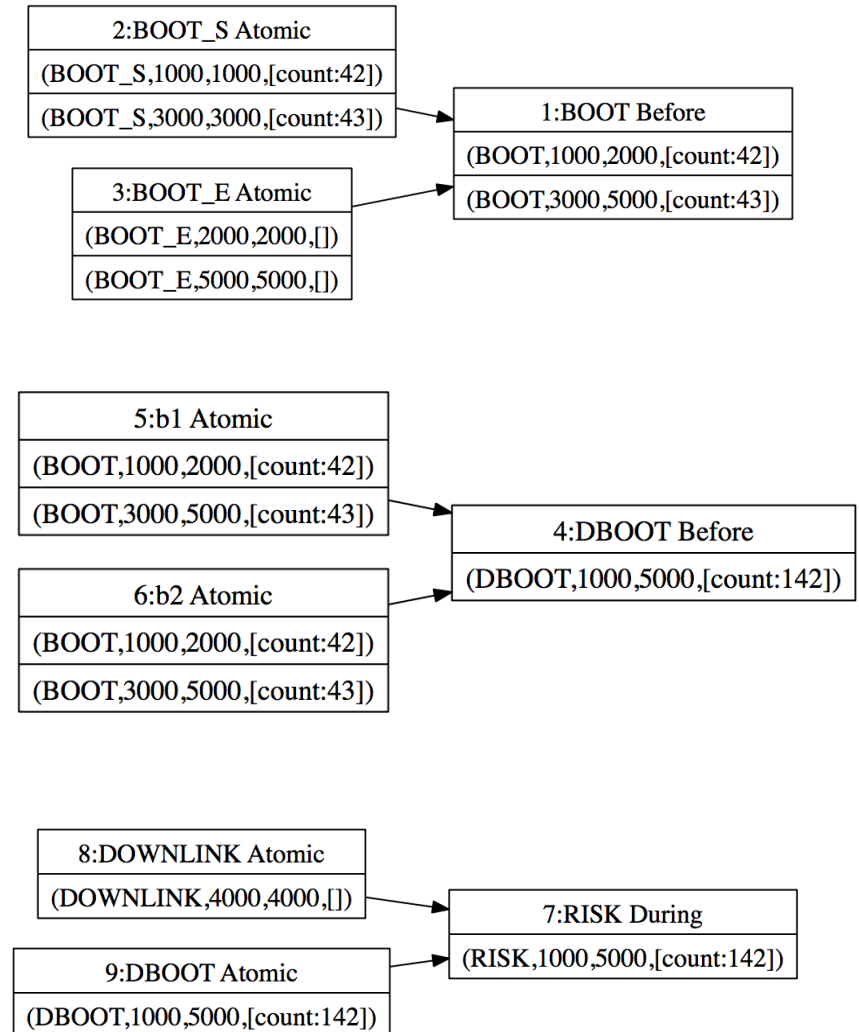


Implementation of Rules as Trees

```
BOOT :- BOOT_S before BOOT_E
      where BOOT_E.begin - BOOT_S.end <= 5000
      map {count -> BOOT_S.count}
```

```
DBOOT :- b1:BOOT before b2:BOOT
        where b2.begin - b1.end <= 10000
        map {count -> b1.count}
```

```
RISK :- DOWNLINK during DBOOT
       map {count -> DBOOT.count}
```



Q

#ABCDEFGHIJKLMNOPQRSTUVWXYZ

display packages only

nfer.repr	hide	focus
Interval		
nfer.system	hide	focus
Nfer		



package **nfer**

nfer is a Scala package for generating abstractions from *event traces*.

Introduction

nfer allows to define intervals over an event trace. The intervals indicate abstractions that for example can be visualized. An interval consists of a name, two time stamps: the beginning and the end of the interval, and a map from variable names (strings) to values, indicating data that the interval carries.

Writing Specifications

As an example, consider a scenario where we want to be informed if a downlink operation occurs during a 5-minute time interval where the flight computer reboots twice. This scenario could cause a potential loss of downlink information. We want to identify the following intervals.

A BOOT represents an interval during which the rover software is rebooting.

A DBOOT (double boot) represents an interval during which the rover reboots twice within a 5-minute timeframe.

A RISK represents an interval during which the rover reboots twice, and during which also attempts to downlink information.

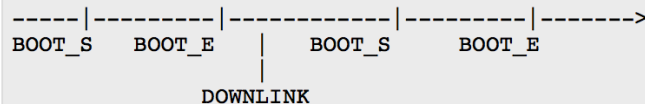
Our objective now is to formalize the definition of such intervals in the nfer specification language, and extract these intervals from a telemetry stream, based on such a specification. Specifically, in this case, we need a formalism for formally defining the following three kinds of intervals.

A BOOT interval starts with a BOOT_S (boot start) event and ends with a BOOT_E (boot end) event.

A DBOOT (double boot) interval consists of two consecutive BOOT intervals, with no more than 5-minutes from the end of the first BOOT interval to the start of the second BOOT interval.

A RISK interval is a DBOOT interval during which a DOWNLINK occurs.

The following time line illustrates a scenario with two BOOTs, hence a double boot, and a downlink occurring during this period.



The intervals can be formalized with the following nfer specification:

```

BOOT :- BOOT_S before BOOT_E
  where BOOT_E.begin - BOOT_S.end <= 5000
  map {count -> BOOT_S.count}
  
```

```

DBOOT :- b1:BOOT before b2:BOOT
  where b2.begin - b1.end <= 10000
  map {count -> b1.count}
  
```

GUI for Entering Rules using a Log

Loaded 31 EVRs from demo_example.csv.

<input type="radio"/>	SEQ_EVR_CMD_DISPATCH_ID	SOL-1387M09:25:01.038	Dispatching sequenced command opcode SEQ_WAIT_FOR from sequence engine #1. Sequence 0x532cb (rems13003) checksum 0x707887ff vers
<input type="radio"/>	SEQ_EVR_BG_END	SOL-1387M09:25:00.312	SEQ background processing completed.
<input checked="" type="radio"/>	SEQ_EVR_RUN_SEQ_START_ID	SOL-1387M09:25:00.433	Sequence File *rems03003* version *0009* running in sequence engine #2* is activating Sequence File *rems00215* version *0009* checksum 0
<input type="radio"/>	SEQ_EVR_CALL_STACK_REPORT_7	SOL-1387M09:25:00.433	Sequence invocation call stack (up to *7* from most recent to least recent): Sequence 0x*500d7 (rems00215)*, Sequence 0x*50bbb (rems03003)*, S
<input type="radio"/>	SEQ_EVR_ENGINE_DISABLE_REQUESTS	SOL-1387M09:25:00.433	Disabling requests for sequence engine #*3*.
<input type="radio"/>	SEQ_EVR_VALIDATE_SEQ_BG_PRE	SOL-1387M09:25:00.433	Sequence client id *3* requesting validation of Sequence File *rems00215* in SEQ BG context.
<input checked="" type="radio"/>	SEQ_EVR_BG_BEGIN	SOL-1387M09:25:00.434	About to perform SEQ background processing.
<input type="radio"/>	SEQ_EVR_VALIDATE_SEQ_START	SOL-1387M09:25:00.434	Sequence client id *3* starting validation of Sequence File *rems00215* at command #*0* command position *0*.
<input checked="" type="radio"/>	SEQ_EVR_VALIDATE_XSUM_COMPUTE	SOL-1387M09:25:00.554	Computed checksum for Sequence File *rems00215* is 0x*856ac345*.
<input type="radio"/>	CMD_EVR_SEQED_CMD_VALID	SOL-1387M09:25:00.555	Valid sequenced command REMS_WAIT_UNTIL_FREE: engine number=0, seconds=520611406, subseconds=655360.
<input type="radio"/>	SEQ_EVR_VALIDATE_SEQ_DONE_SUCC	SOL-1387M09:25:00.555	Sequence client id *3* completed validation of Sequence File *rems00215* Sequence 0x*500d7 (rems00215)* checksum 0x*856ac345* with SUCC
<input type="radio"/>	SEQ_EVR_ENG_VALIDATE_DONE_SUCC	SOL-1387M09:25:00.555	Sequence engine #*3* passed validation for Sequence File *rems00215*.
<input checked="" type="radio"/>	SEQ_EVR_ENGINE_ENABLE_REQUESTS	SOL-1387M09:25:00.555	Enabling requests for sequence engine #*3*.
<input checked="" type="radio"/>	SEQ_EVR_CMD_DISPATCH_ID	SOL-1387M09:25:00.555	Dispatching sequenced command opcode REMS_WAIT_UNTIL_FREE from sequence engine #3. Sequence 0x500d7 (rems00215) checksum 0x85
<input type="radio"/>	SEQ_EVR_BG_END	SOL-1387M09:25:00.556	SEQ background processing completed.
<input type="radio"/>	SEQ_EVR_COPY_DDI_NOT_IMMEDIATE	SOL-1387M09:25:00.433	Copying Sequence Engine state from *2* to *3*.
<input type="radio"/>	SEQ_EVR_WAIT_UNTIL_TIMER_STARTED_ID	SOL-1387M09:25:01.039	Sequence engine #*1* will wait until *520613327*- *1029816320*.
<input checked="" type="radio"/>	SEQ_EVR_CMD_DISPATCH_ID	SOL-1387M09:25:01.222	Dispatching sequenced command opcode SEQ_NO_EARLIER_THAN from sequence engine #0. Sequence 0x78182 (mstr00386) checksum 0xc77
<input type="radio"/>	SEQ_EVR_WAIT_UNTIL_TIMER_STARTED_ID	SOL-1387M09:25:01.223	Sequence engine #*0* will wait until *520613656*- *0*.
<input type="radio"/>	HGA_EVR_UPDATING_POINTING	SOL-1387M09:25:03.030	Updating HGA Earth pointing: Earth direction in rover mechanical frame is (*-0.299463*, *-0.220673*, *-0.928238*). Moving HGA azimuth to *1
<input type="radio"/>	MOT_EVR_STARTING_SERVO	SOL-1387M09:25:03.195	Starting servo: motor HGA_AZ mode POS_ABS pos 1.78565 servo_pos 1.77817 rate 0.1 climit 1.300 vlimit 31.0.
<input type="radio"/>	MOT_EVR_STARTING_SERVO	SOL-1387M09:25:03.195	Starting servo: motor HGA_EL mode POS_ABS pos 0.384529 servo_pos 0.383226 rate 0.02 climit 1.300 vlimit 31.0.
<input type="radio"/>	SDST_EVR_SENDING_CMD_ID	SOL-1387M09:25:25.123	Write to 1553 subaddress 29, num words = 1, buf[0] = 0x0030, buf[1] = 0x0000
<input type="radio"/>	SDST_EVR_SENDING_CBIT_CMD	SOL-1387M09:25:25.123	Sending *CBIT* command to SDST *ROVER*
<input type="radio"/>	HGA_EVR_UPDATING_POINTING	SOL-1387M09:25:46.826	Updating HGA Earth pointing: Earth direction in rover mechanical frame is (*-0.300155*, *-0.217714*, *-0.928713*). Moving HGA azimuth to *1
<input type="radio"/>	MOT_EVR_STARTING_SERVO	SOL-1387M09:25:46.991	Starting servo: motor HGA_AZ mode POS_ABS pos 1.77817 servo_pos 1.77066 rate 0.1 climit 1.300 vlimit 31.0.
<input type="radio"/>	MOT_EVR_STARTING_SERVO	SOL-1387M09:25:46.991	Starting servo: motor HGA_EL mode POS_ABS pos 0.383182 servo_pos 0.381943 rate 0.02 climit 1.300 vlimit 31.0.
<input type="radio"/>	SEQ_EVR_WAIT_CMD_COMPLETED_SUCCESS	SOL-1387M09:25:58.947	Successfully completed sequenced command opcode REMS_WAIT_UNTIL_FREE dispatched from sequence engine #3. Sequence 0x500d7 (rems0
<input checked="" type="radio"/>	SEQ_EVR_REPORT_CMPLT_STATUS_ID	SOL-1387M09:25:58.947	Sequence File *rems00215* (Sequence 0x*500d7 (rems00215)*, version *0009*, checksum 0x*856ac345*) running in sequence engine #*3* has c
<input type="radio"/>	SEQ_EVR_TERMINATION_ID	SOL-1387M09:25:58.948	Sequence engine #*3* with Sequence 0x*500d7 (rems00215)* has been terminated and set to INACTIVE.
<input type="radio"/>	SEQ_EVR_WAIT_CMD_COMPLETED_SUCCESS	SOL-1387M09:25:58.948	Successfully completed sequenced command opcode SEQ_RUN dispatched from sequence engine #2. Sequence 0x50bbb (rems03003) checksum 0

seqOk
SEQ_EVR_RUN_SEQ_START_ID before
SEQ_EVR_REPORT_CMPLT_STATUS_ID
Interval: 53.5s to 63.5s (was 58.5140 in sample)

Rule 1
SEQ_EVR_BG_BEGIN before
SEQ_EVR_VALIDATE_XSUM_COMPUTE
Interval: 0.0s to 5.1s (was 0.1200 in sample)

Rule 2
SEQ_EVR_ENGINE_ENABLE_REQUESTS before
SEQ_EVR_CMD_DISPATCH_ID
Interval: 0.0s to 5.0s (was 0.0000 in sample)

Rule 3
SEQ_EVR_CMD_DISPATCH_ID before
SEQ_EVR_CMD_DISPATCH_ID
Interval: 0.0s to 5.7s (was 0.6670 in sample)

Export Rules JSON

END